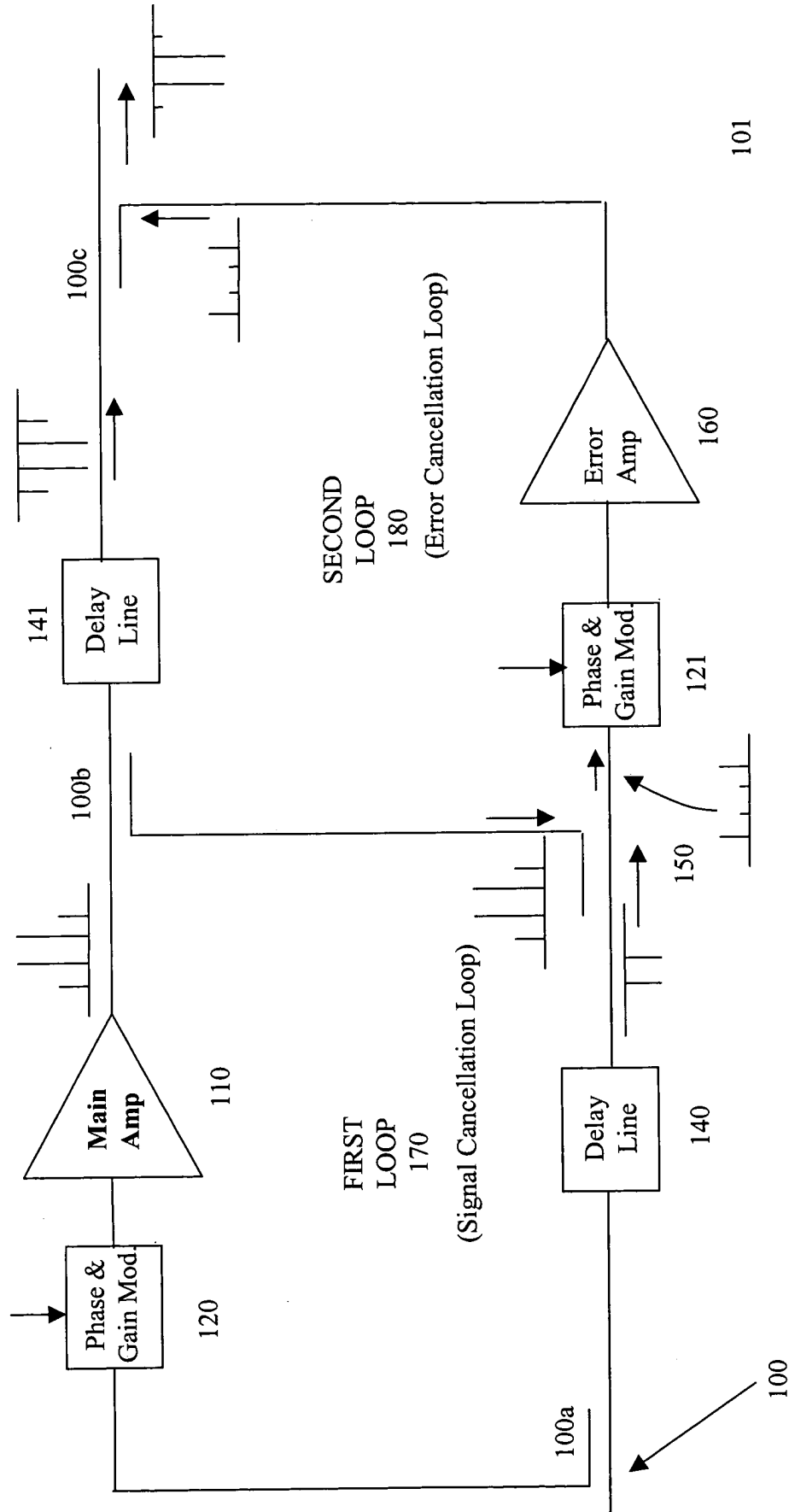


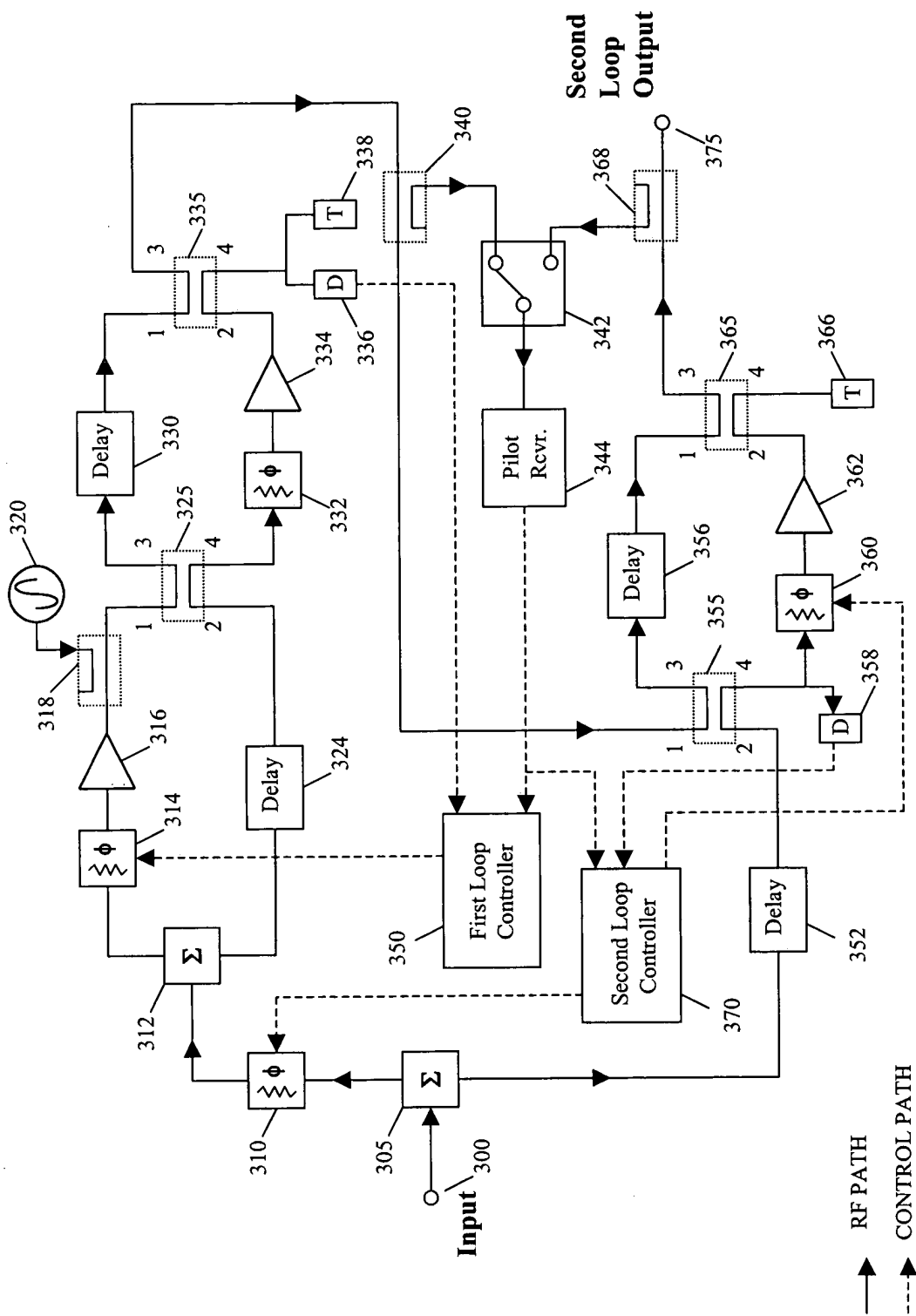


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PRIOR ART

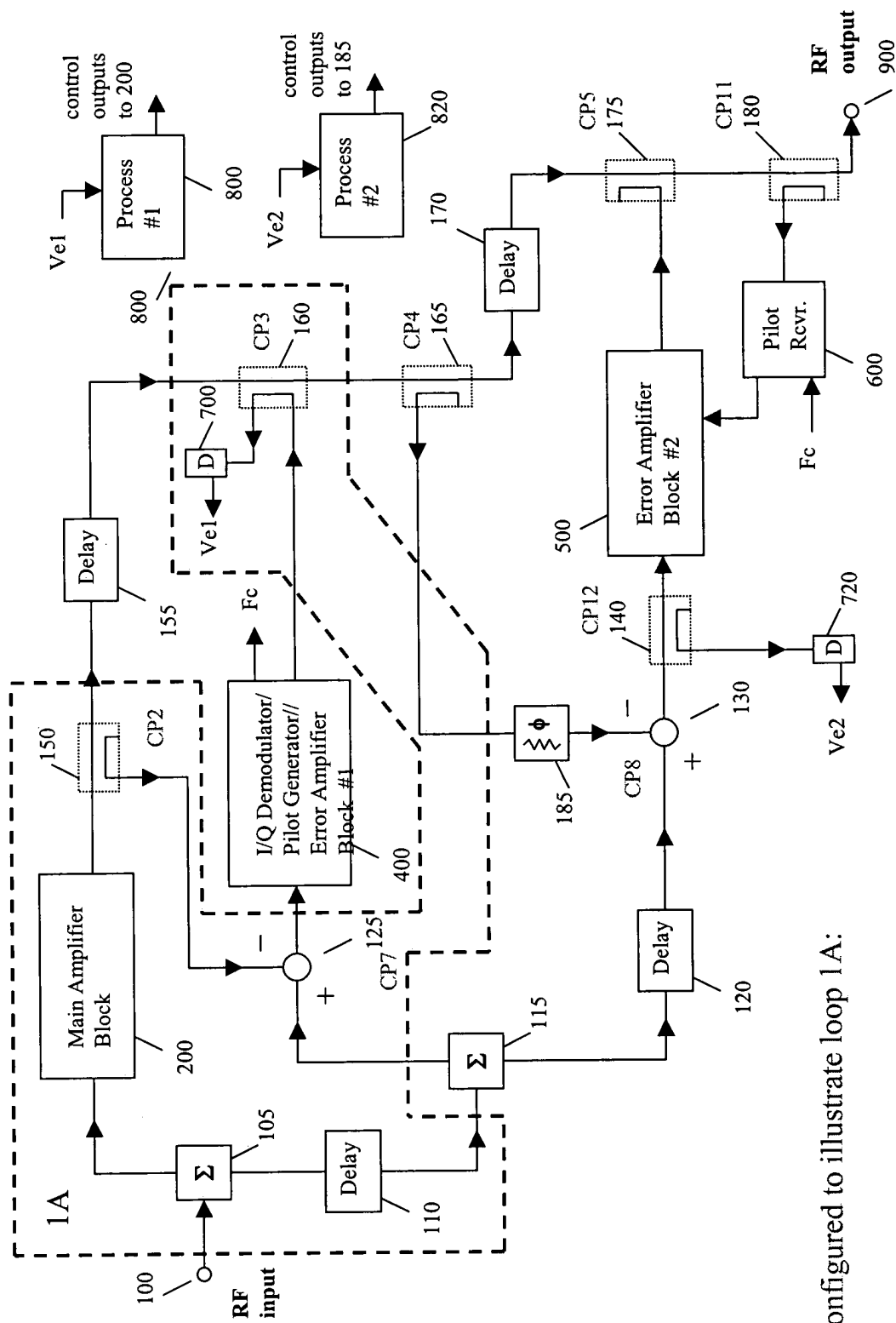
FIG. 1



PRIOR ART  
 FIG. 2



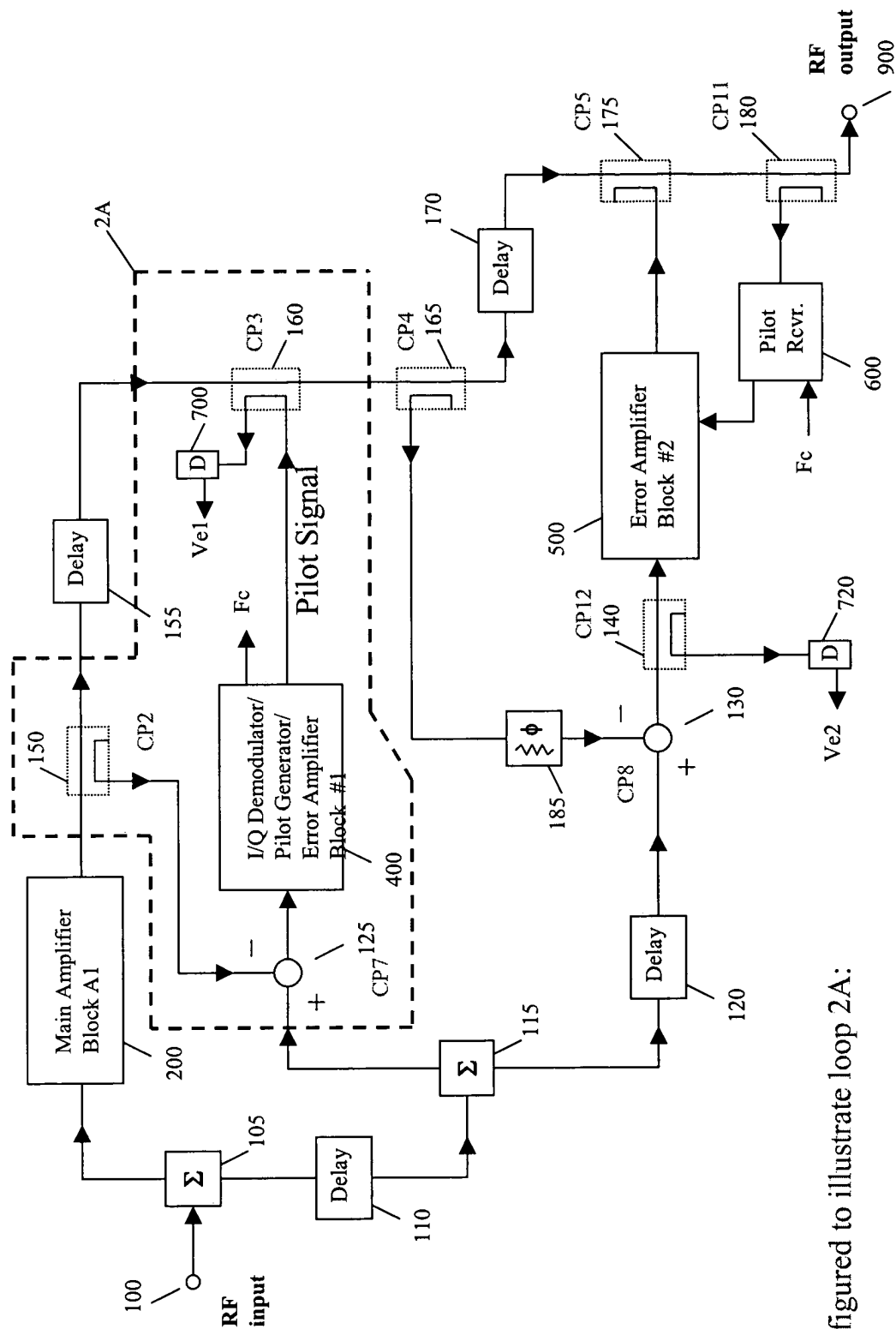
FIG. 3



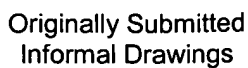
configured to illustrate loop 1A:

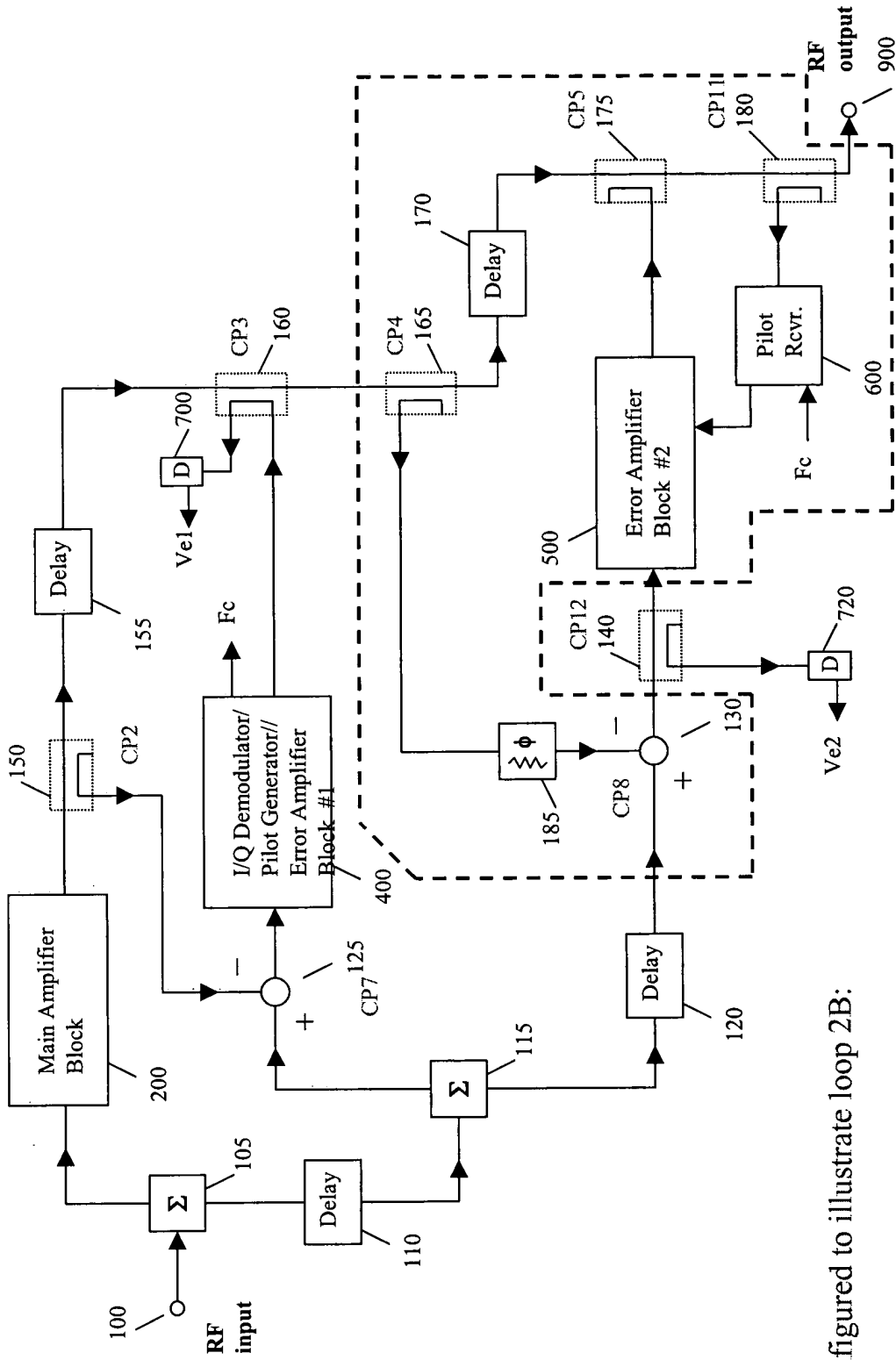
FIG. 4

FIG. 5



configured to illustrate loop 2A:





configured to illustrate loop 2B:

FIG. 7







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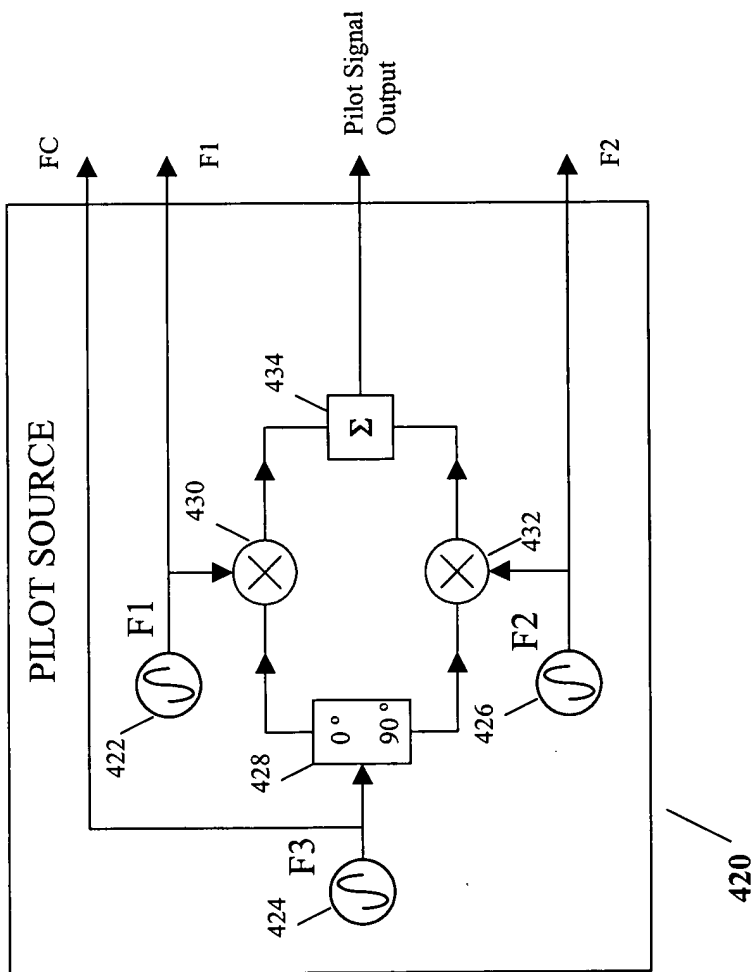
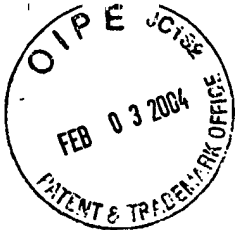


FIG. 9



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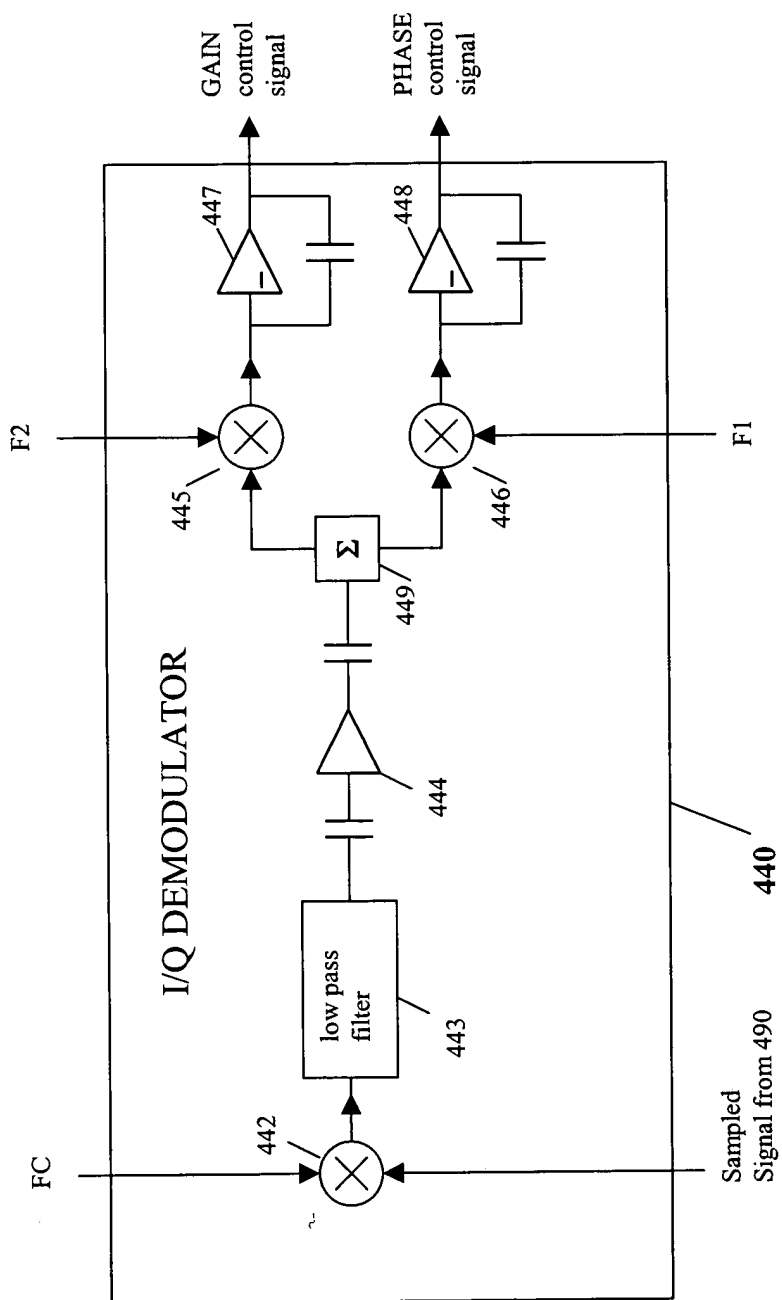


FIG. 10





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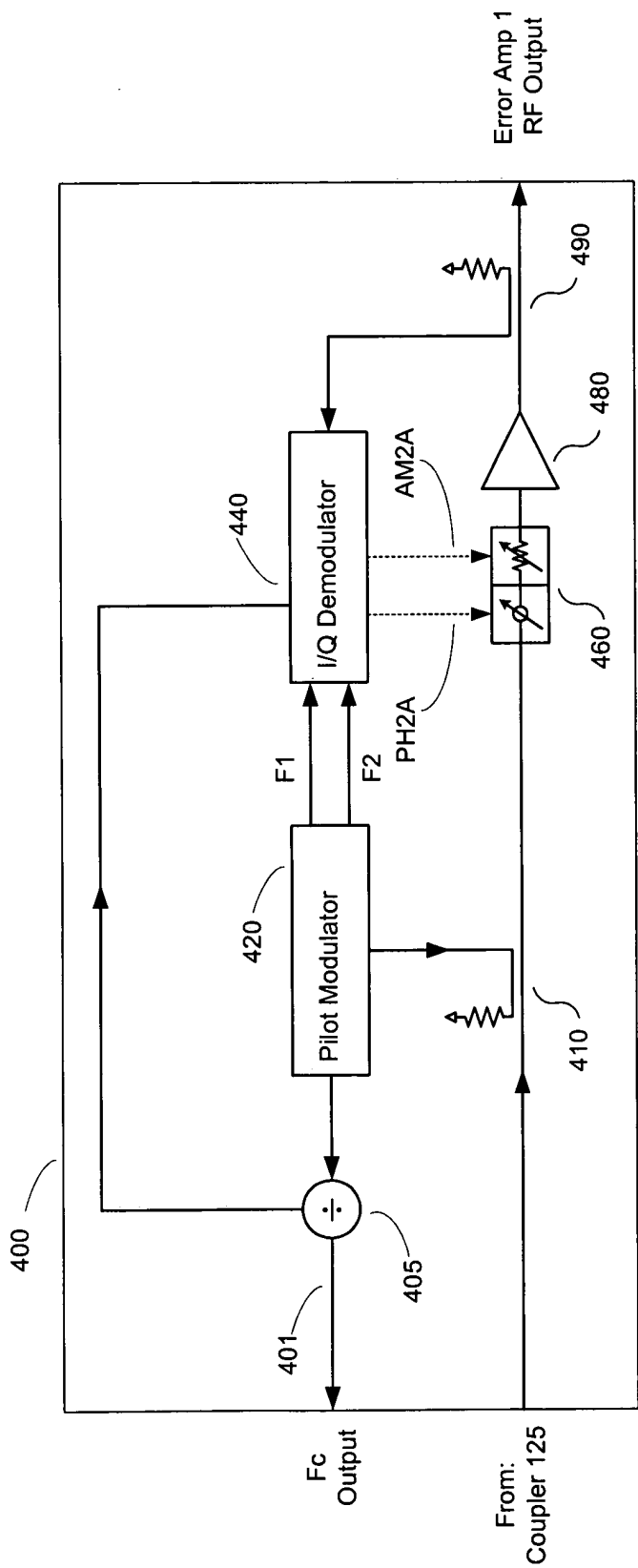


FIG. 12

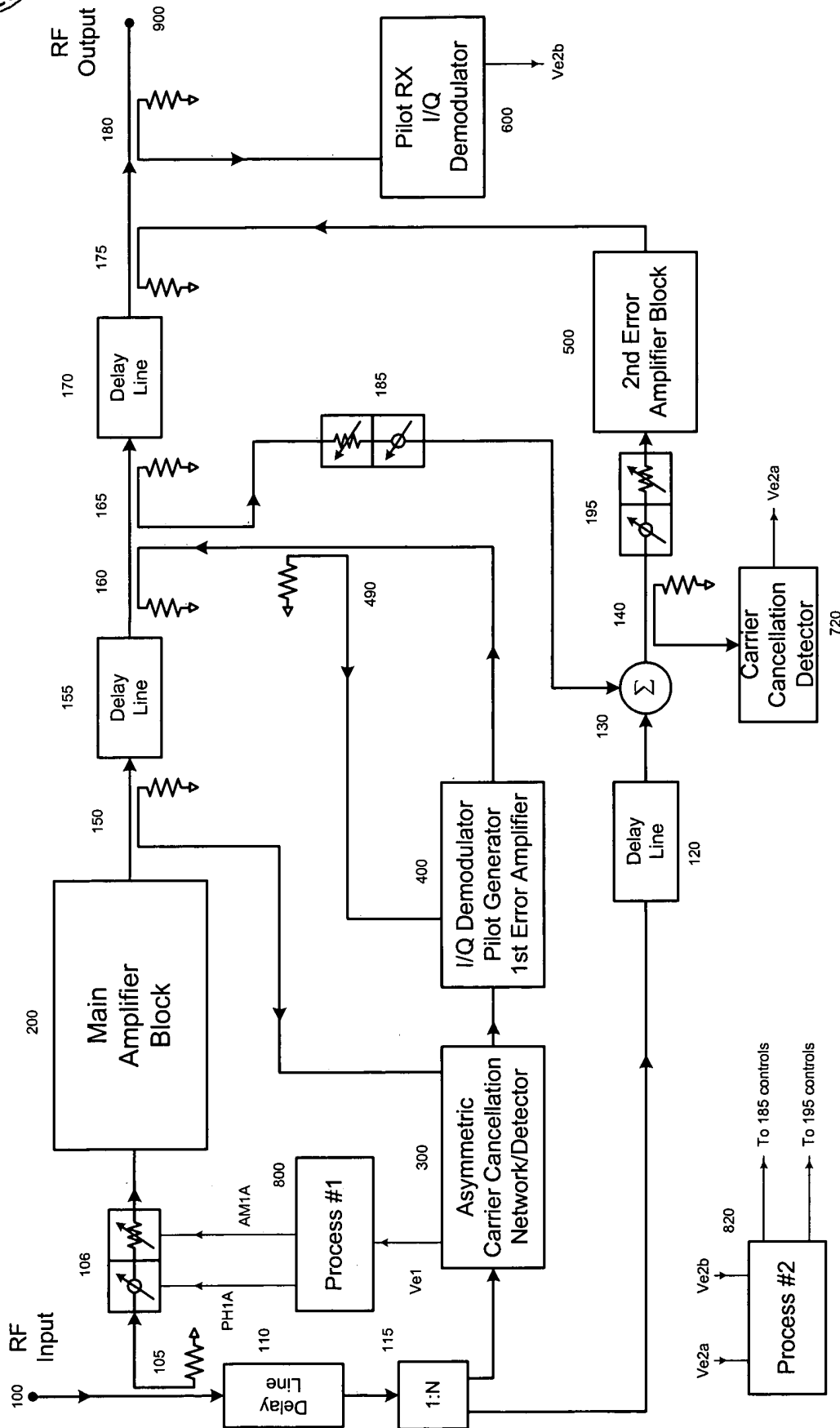


FIG. 13A

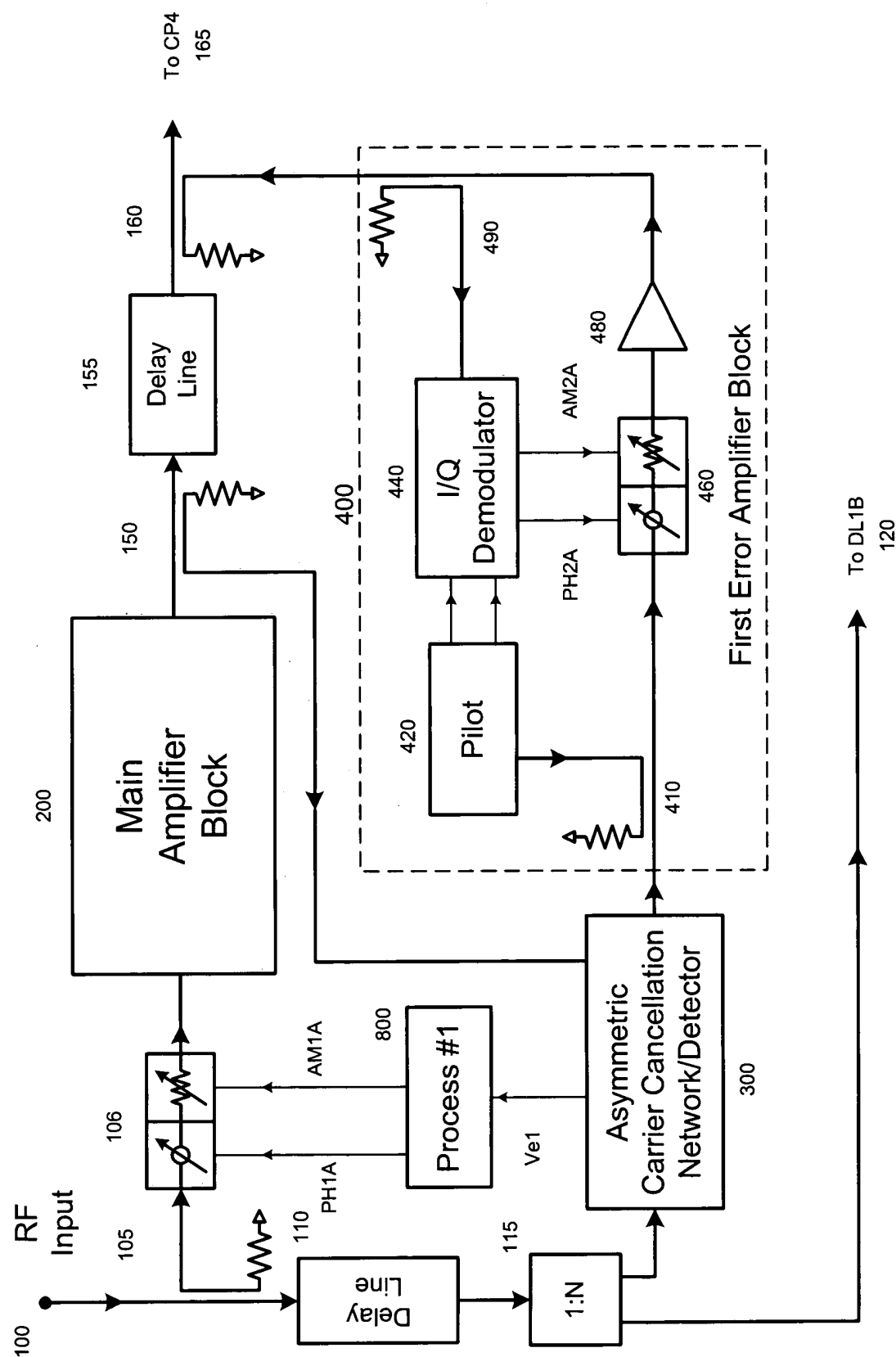


FIG. 13B

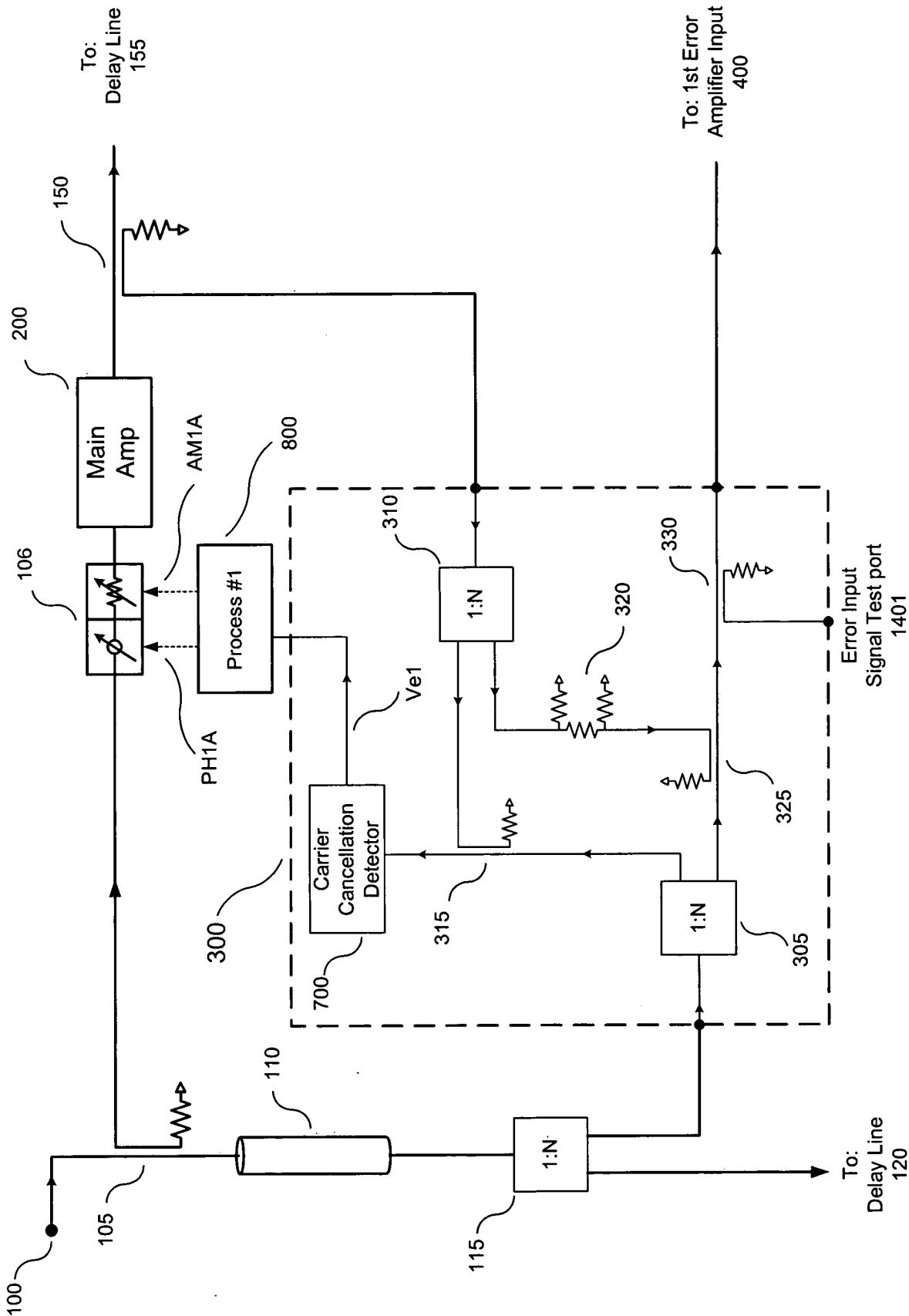


FIG. 14

# 1<sup>st</sup> Error Amplifier Stabilization

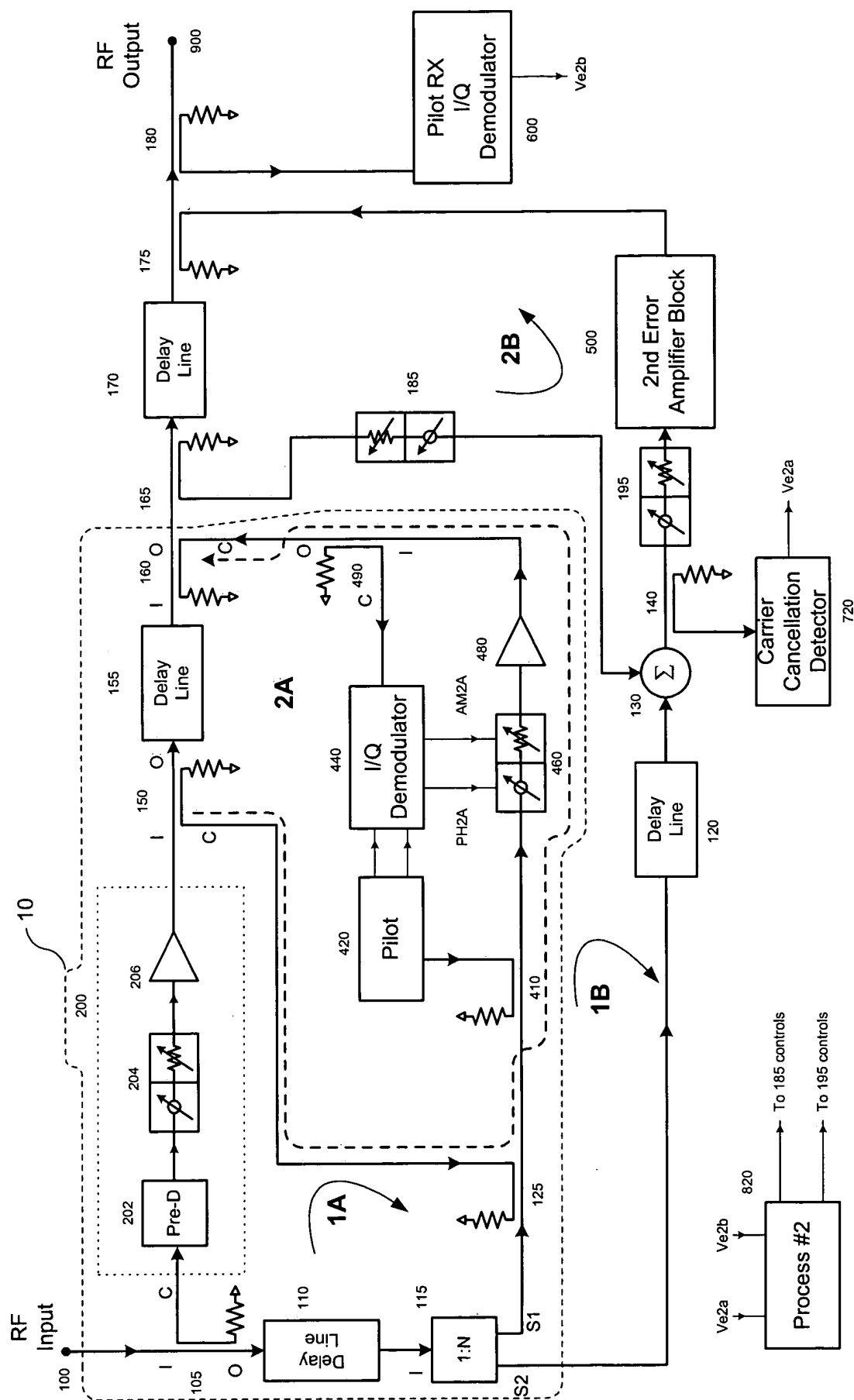


FIG. 15





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# Error Amplifier Stabilization

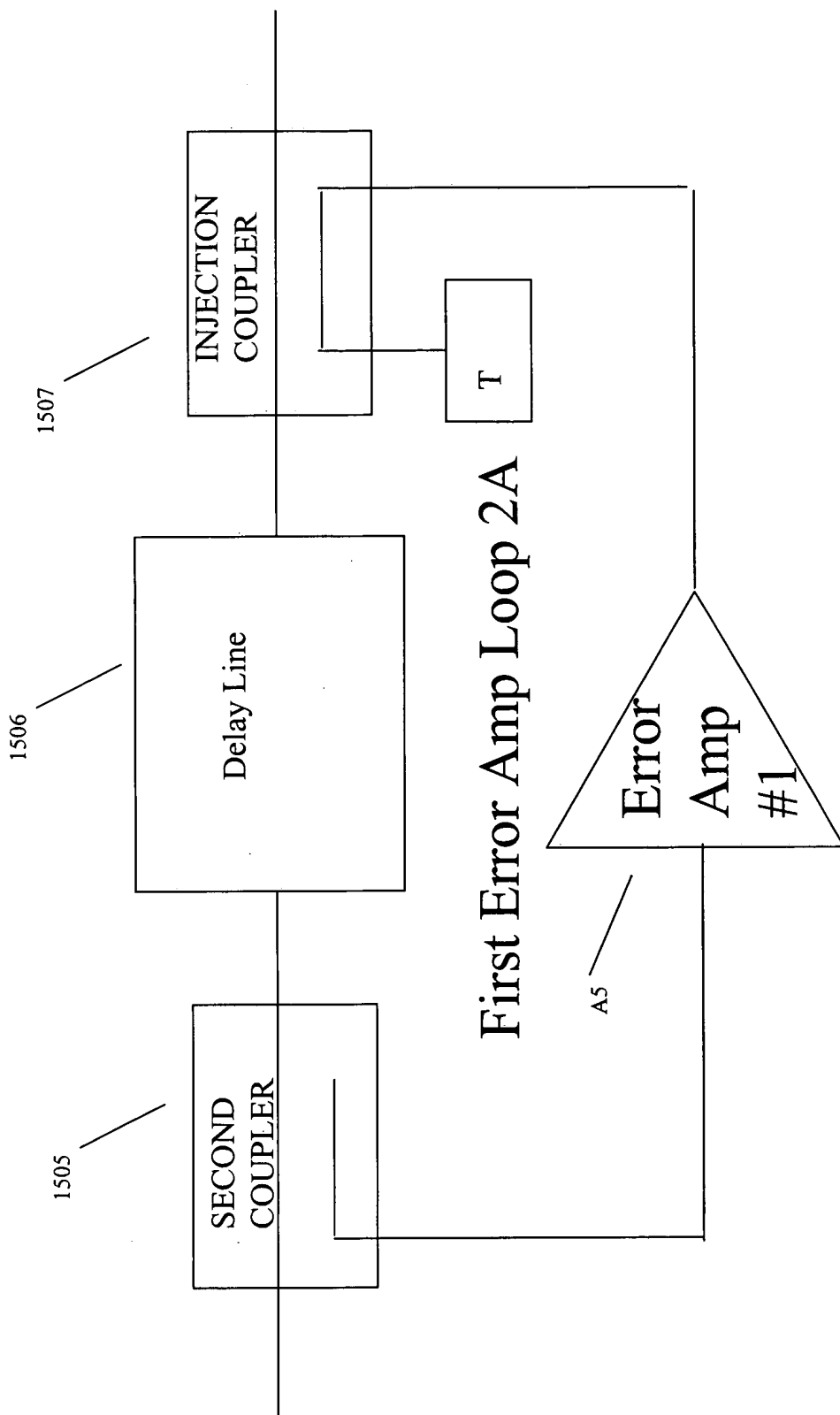


FIG. 16



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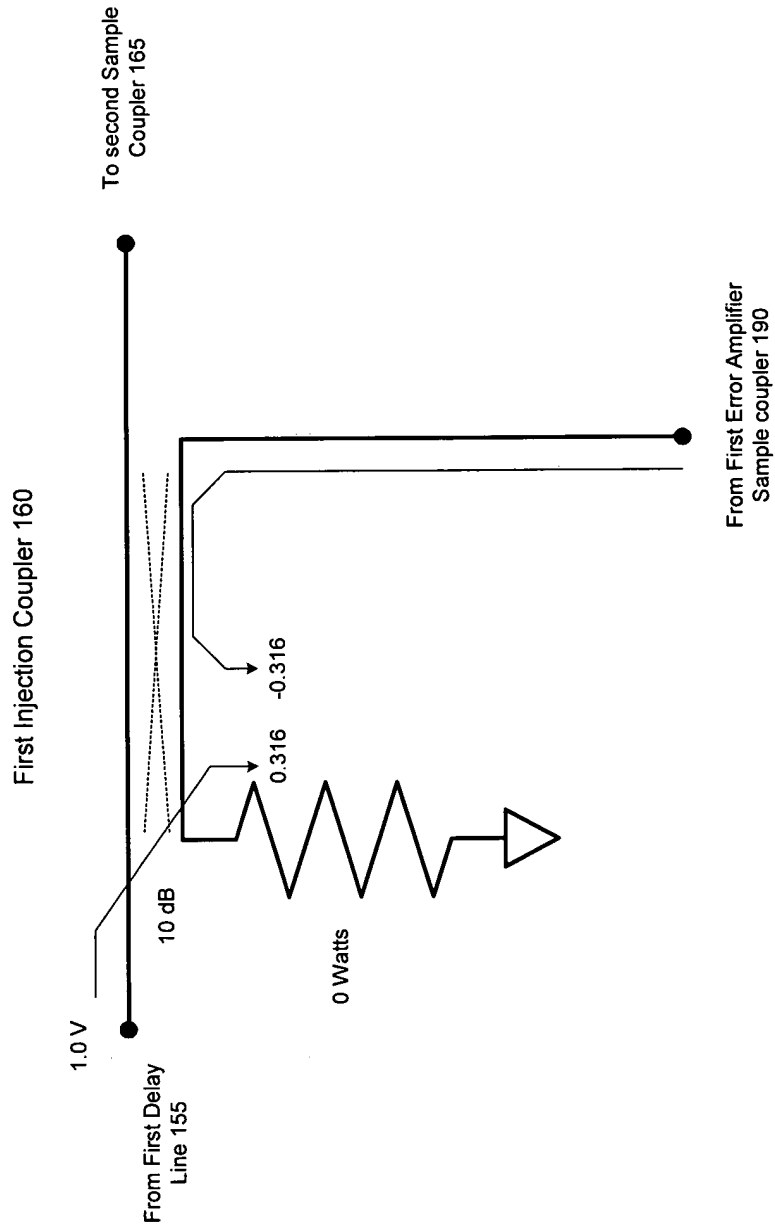
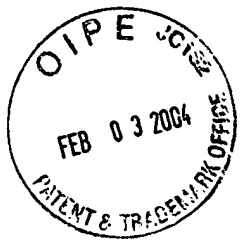


FIG. 17



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# Carrier Re-Injection

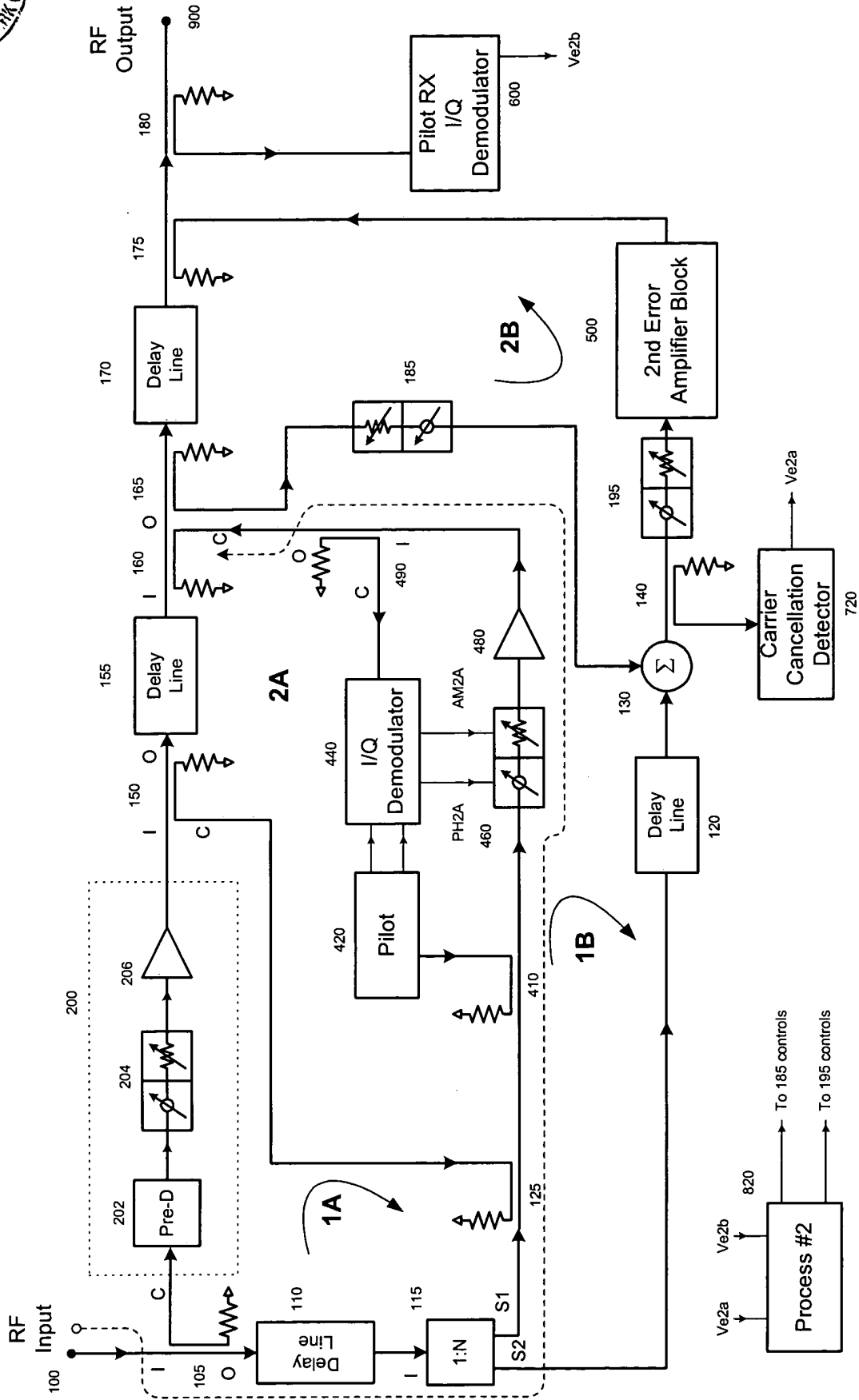
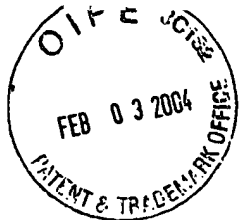


FIG. 18

# Non-Overlapping Loop Adjustment



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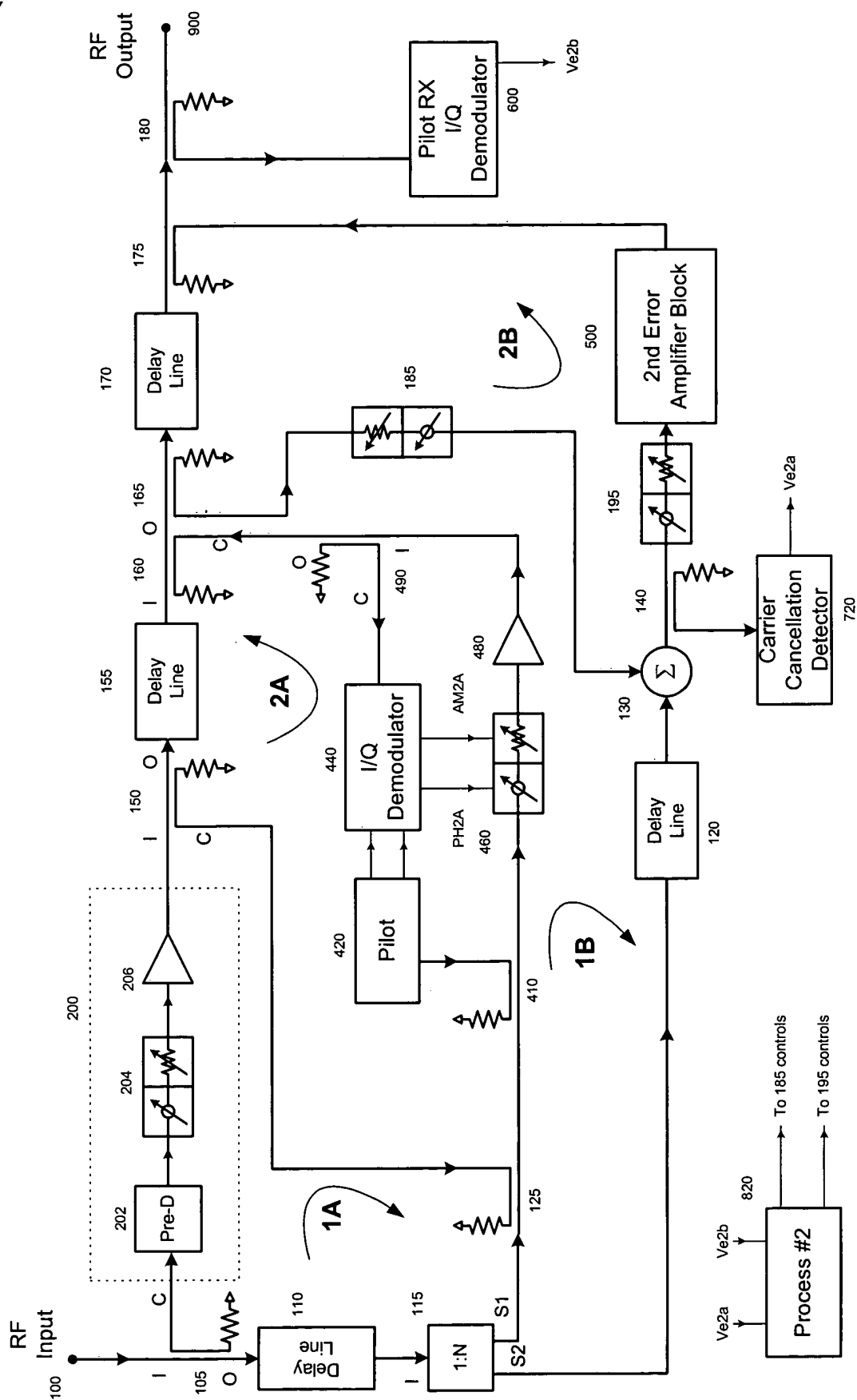


FIG. 19